

JUN 27 2006

HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P.O. Box 272400
Fort Collins, Colorado 80527-2400

PATENT APPLICATION

ATTORNEY DOCKET NO. 200311777-1

Inventor(s): S. Brandon Keller et al.

Confirmation No.: 7814

Application No.: 10/803,693

Examiner: Phallaka Kik

Filing Date: March 18, 2004

Group Art Unit: 2825

Title: **System and Method for Flattening Hierarchical Designs in VLSI Circuit Analysis Tools**Mail Stop Amendment
Commissioner For Patents
PO Box 1450
Alexandria, VA 22313-1450**TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT**

Transmitted herewith is/are the following in the above-identified application:

- ☒ Response/Amendment
☐ New fee as calculated below
☒ No additional fee
☐ Other

- ☐ Petition to extend time to respond
☐ Supplemental Declaration

Fee\$

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS	33	MINUS	33	= 0	X \$50	\$ 0
INDEP. CLAIMS	6	MINUS	6	= 0	X \$200	\$ 0
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						+ \$360 \$ 0
EXTENSION FEE	<input type="checkbox"/> 1st Month \$120	<input type="checkbox"/> 2nd Month \$450	<input type="checkbox"/> 3rd Month \$1020	<input type="checkbox"/> 4th Month \$1590	\$ 0	
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any over payment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

I hereby certify that this paper is being
transmitted to the Patent and Trademark Office
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Date of facsimile: June 27, 2006

Typed Name: Shreen K. Danamraj

Signature: 

Total Pages: 23 + 1

Respectfully submitted,

S. Brandon Keller et al.

By 

Shreen K. Danamraj

Attorney/Agent for Applicant(s)

Reg No. : 41,696

Date : June 27, 2006

Telephone : (214)750-5886

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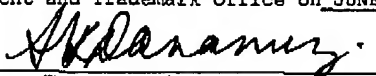
PATENT APPLICATION
DOCKET NO.: 200311777-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: \$
 \$
 S. Brandon Keller et al. \$ Confirmation No.: 7814
 \$
Appl. No.: 10/803,693 \$ Art Unit: 2825
 \$
Filed: March 18, 2004 \$ Examiner: Phallaka Kik

For: SYSTEM AND METHOD FOR FLATTENING HIERARCHICAL DESIGNS IN VLSI
CIRCUIT ANALYSIS TOOLS

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Patent and Trademark Office on JUNE 27, 2006.

Shreen K. Danamraj

RESPONSE

Dear Sir:

Responsive to the Office Action of March 27, 2006, the shortened statutory period for response thereto having been set to expire on June 27, 2006, reconsideration of the Action and allowance of the present application are respectfully requested and are believed to be appropriate in view of the following amendments and remarks: